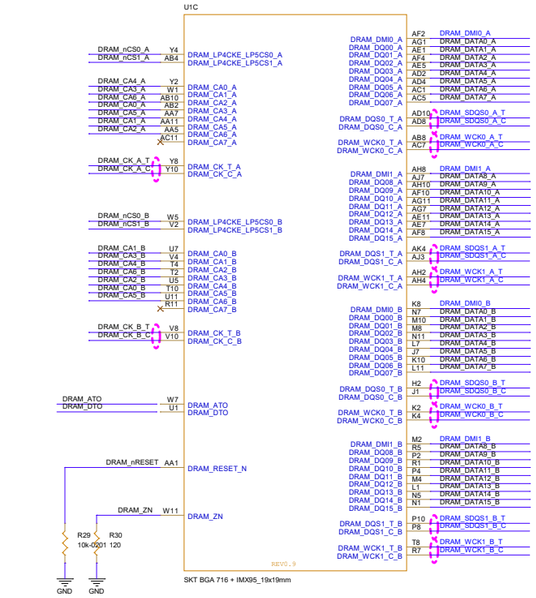
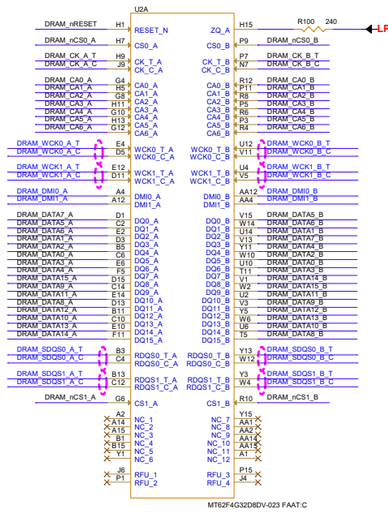
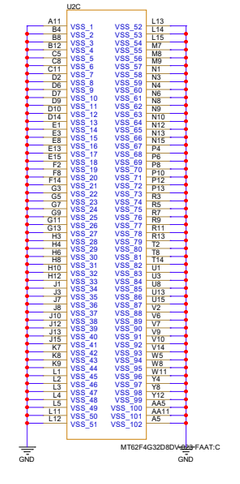
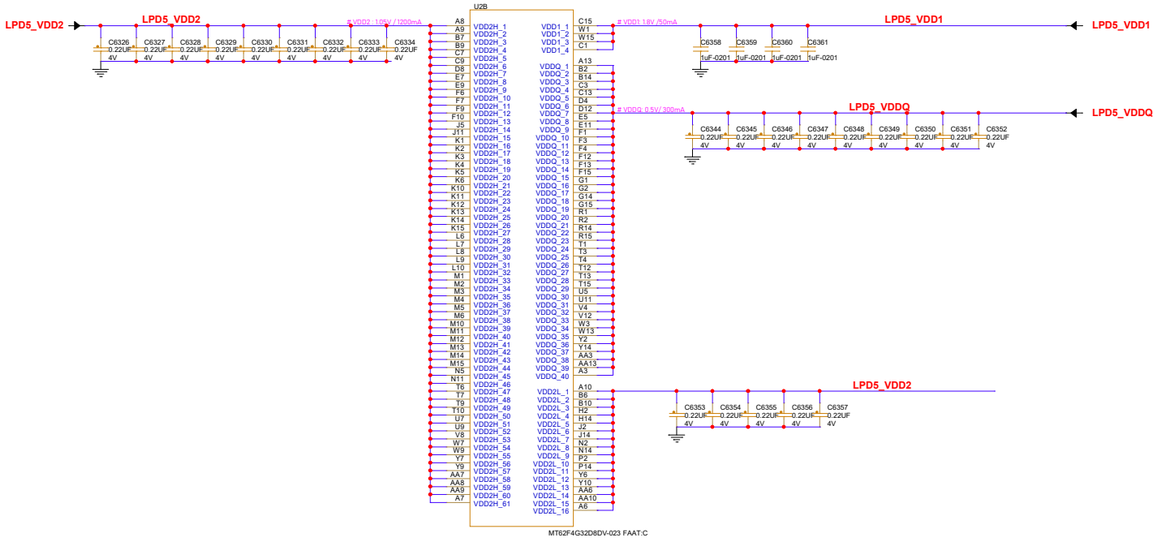



Note: net names are re-arranged (swizzled) to allow for optimised routing. CA net name follows Memory device name. DQ net name follows CPU signal name.

80Ohm



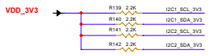



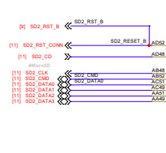

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|---|---|---|
| D | OCP-8040-Mezz | 0.99 |
| Date: Tuesday, March 24, 2026 | Sheet 1 of 4 | |

# i.MX95 IO/PHY

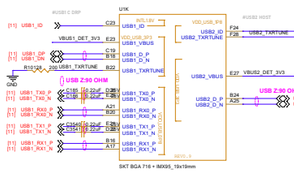


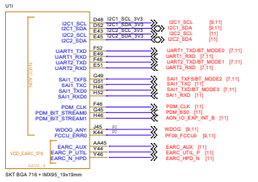



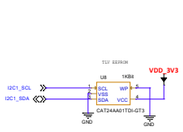



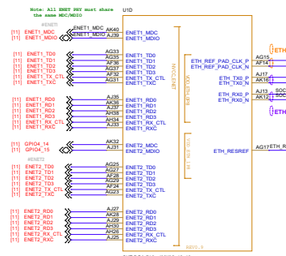


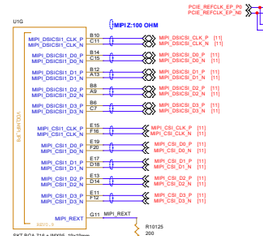


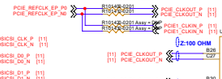


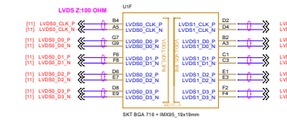



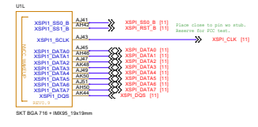



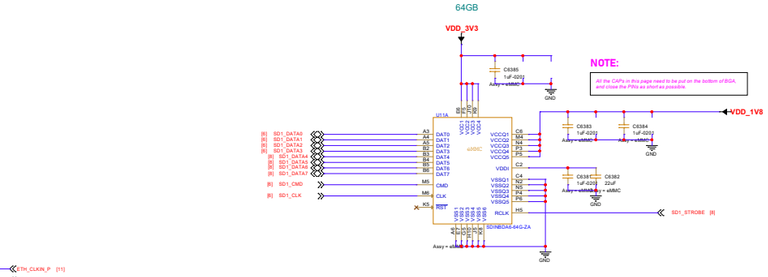



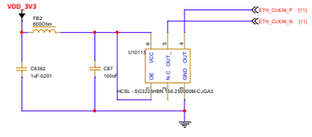



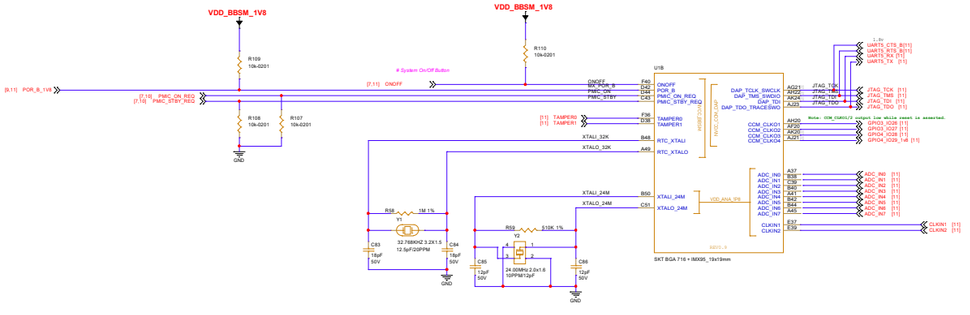





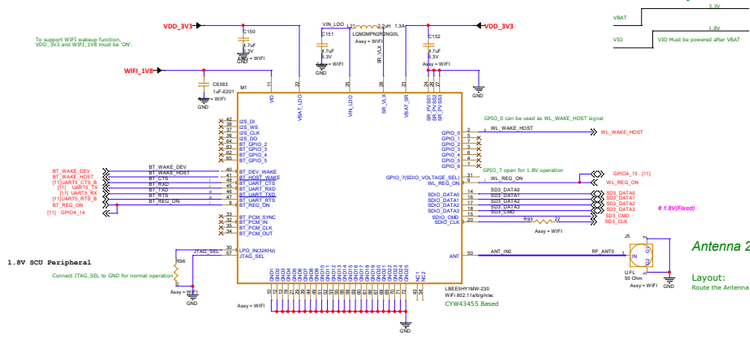





# 2.4G/5G WIFI/BT Module




## Power Sequence




## Antenna 2.4G/5G

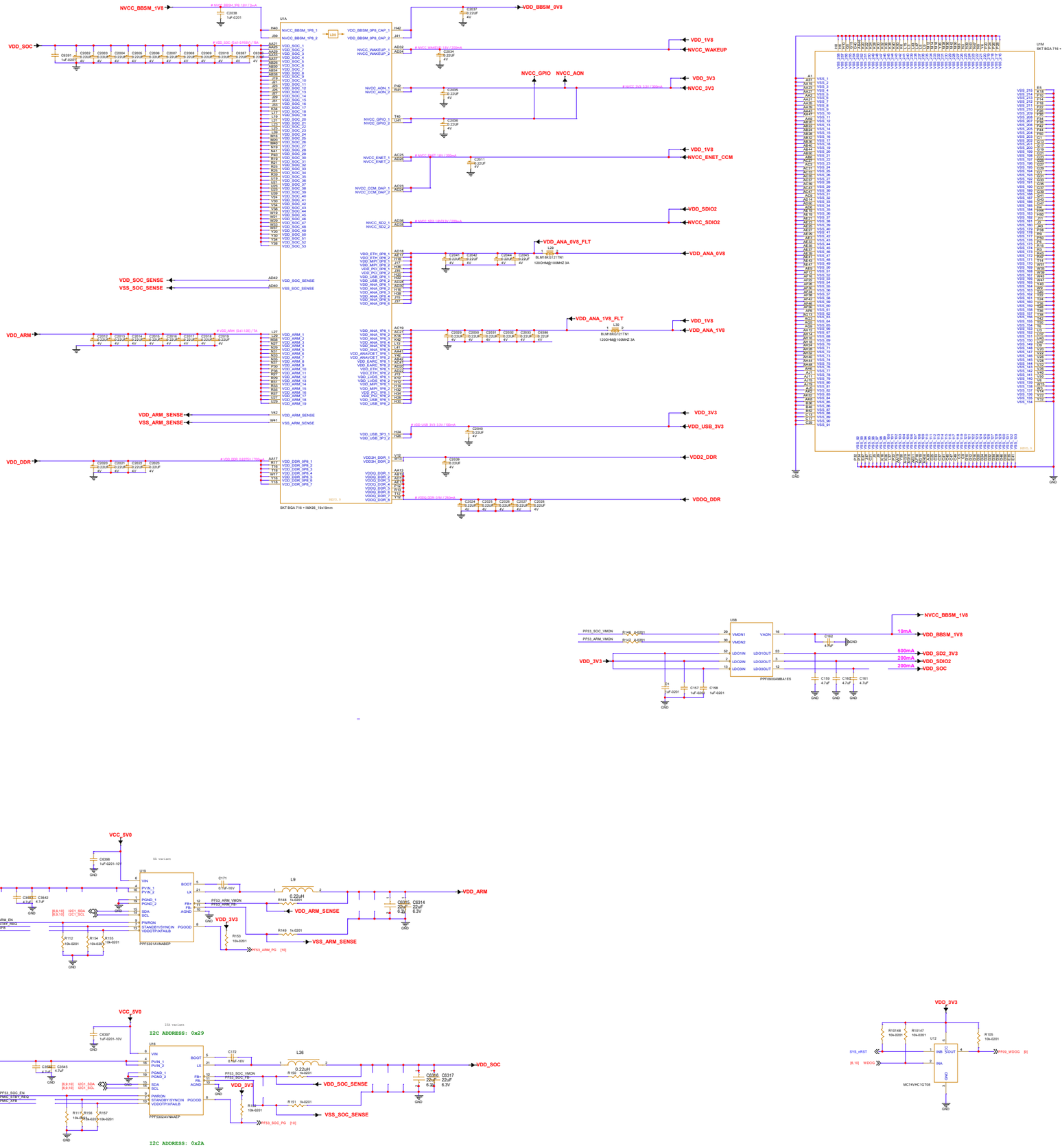
Layout:
Route the Antenna signals as 50 ohm.




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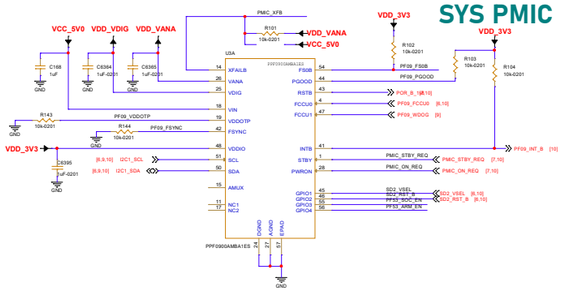
| Id | Rev | Title | Size |
|---|---|---|---|
| 6 | | IMX95 SOM | 1.1 |
| Date: | Sunday, March 29, 2026 | Sheet | 2 of 4 |

# SYS PMIC



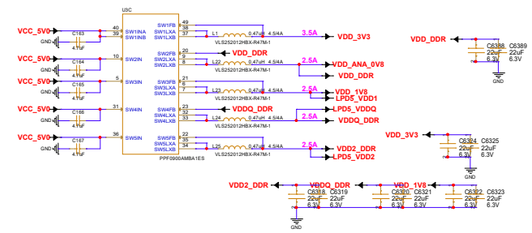



















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|---|---|---|
| E | OCP-8040-Mezz | 0.99 |
| Date: | Friday, September 15, 2023 | Sheet 3 of 4 |

# AQR113C XFI 10GbE PHY

10G PHY Power-up sequence -
0.7v and 1.0v
2.0v and 1.8v and 3.3v

Boot selection BOOT_MODE[3..0] -
1000 - Internal fuses
1001 - Serial downloader
1010 - eMMC
1011 - SD2

PHY Addresses -
0 - 1Gb PHY #1
1 - 1Gb PHY #2
8 - 10Gb PHYBoot selection BOOT_MODE[3..0] -

1.8V --> 3.3V

1.1V 1A ETHERNET PHY POWER SUPPLY

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|---|---|---|
| C | LX4XS SOM | 0.09 |
| Date: | Tuesday, March 24, 2020 | Sheet 4 of 6 |