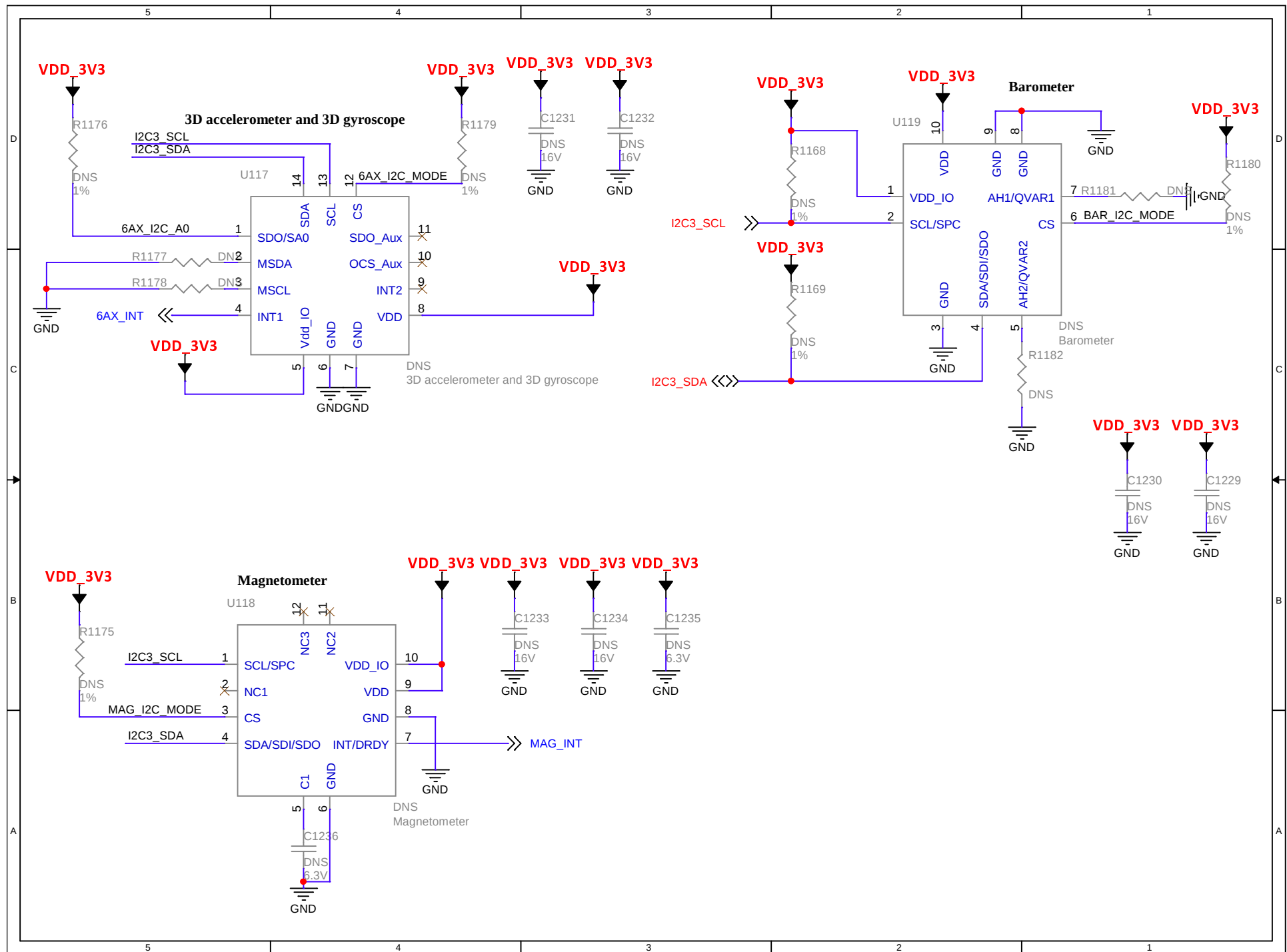




Rev. 1.0	Prototype Version
Rev. 1.1	1. Replaced Toshiba's SSR with Omron (RE1, RE2) 2. Replaced TI's LDO with ADI (U114) 3. Replaced TI's Buck with ADI (U113) 4. Replaced TI's AXC level translator with TI'S AVC (U8) 5. Connecting 5V to OTG2_VBUS as an assembly option (R1165) to support the LTE modem 6. Adding power switch to USB_OTG1. Can be baypassed 7. Adding 3 new sensors 8. Move WL_REG_ON to J9-16 (SPI3_SDI) 9. Fixing FLAXCAN0/1 positions on J16 10. Adding options to the switch boot 11. Switching between RTS and CTS on the WI-FI modem 12. Adding RTC option U116 13. VCCI Power of IO Extender changed to 3V3, C1219 is out 14. No reset signal for IO extender (U2). Connected to J9-56 for future use 15. IO extender interrupt changed to J9 -69 (MCLK_OUT0) 16. Adding a 32KHz oscilator to support WI-FI
Rev. 2.0	1. Replaced the MATE-X connector with 3 u.FL connectors
Rev. 2.0.2	2. Taking L112 and R1162 out of the BOM to enable SW control of the USB power 3. Adding R61 to the BOM (FTDI RX PU) 4. Replace positions of R1115 and R1116 5. Changing D6 value to support 36V power in
Rev. 2.0.3	6. Adding RTC to the BOM

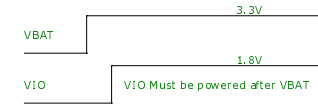


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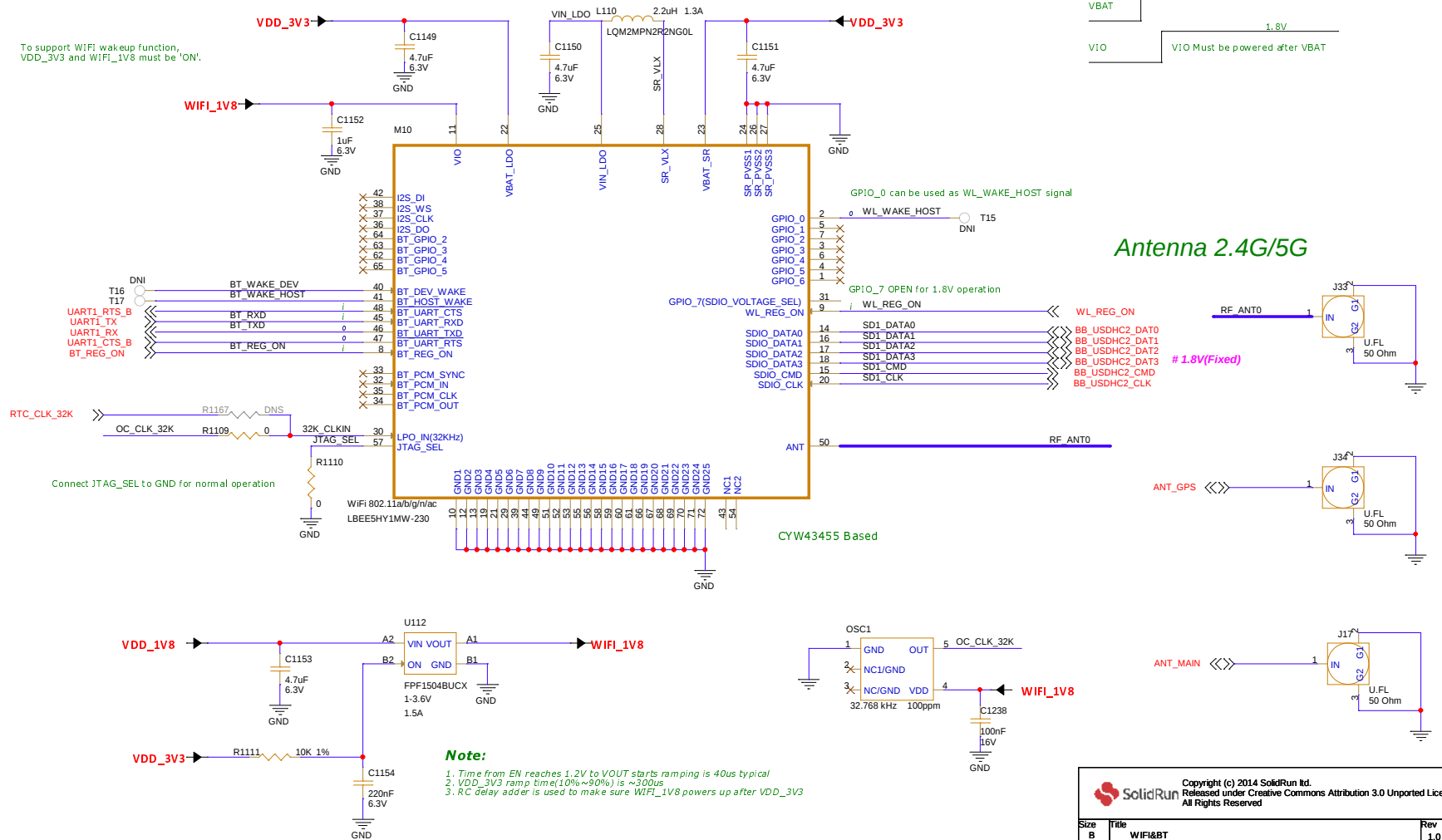
Size A	Title HISTORY	Rev 1.0
Date:	Tuesday, November 28, 2023	Sheet of

2.4G/5G WIFI/BT Module

Power Sequence



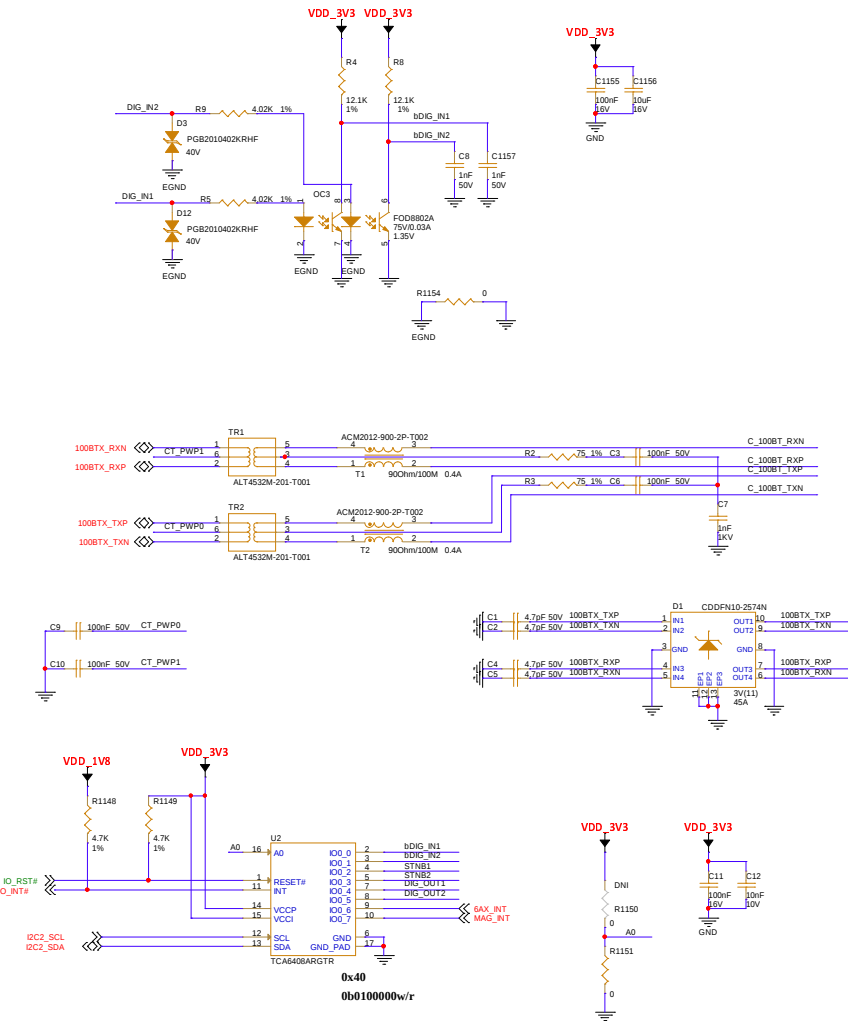
To support WIFI wakeup function, VDD_3V3 and WIFI_1V8 must be 'ON'.



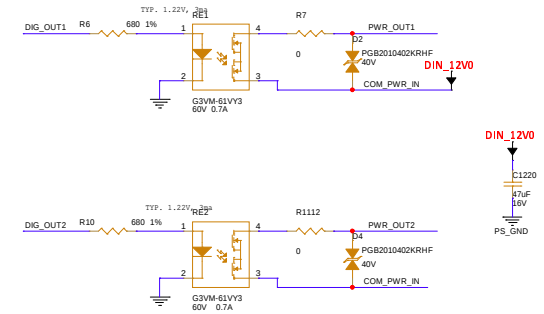
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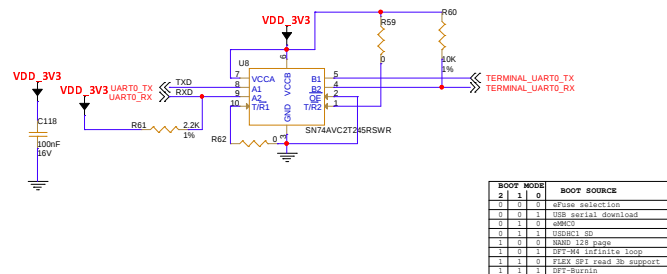
Size	Title	Rev
B	WIFI&BT	1.0
Date:	Sunday, October 30, 2022	Sheet 2 of 7

Digital In



Digital Out





Boot Selection

