ClearFog LX2162A
SolidRun Ltd.

Revision History

Rev#	Date	Owner	Description
1.0	Feb 2022	IlanB	First Release

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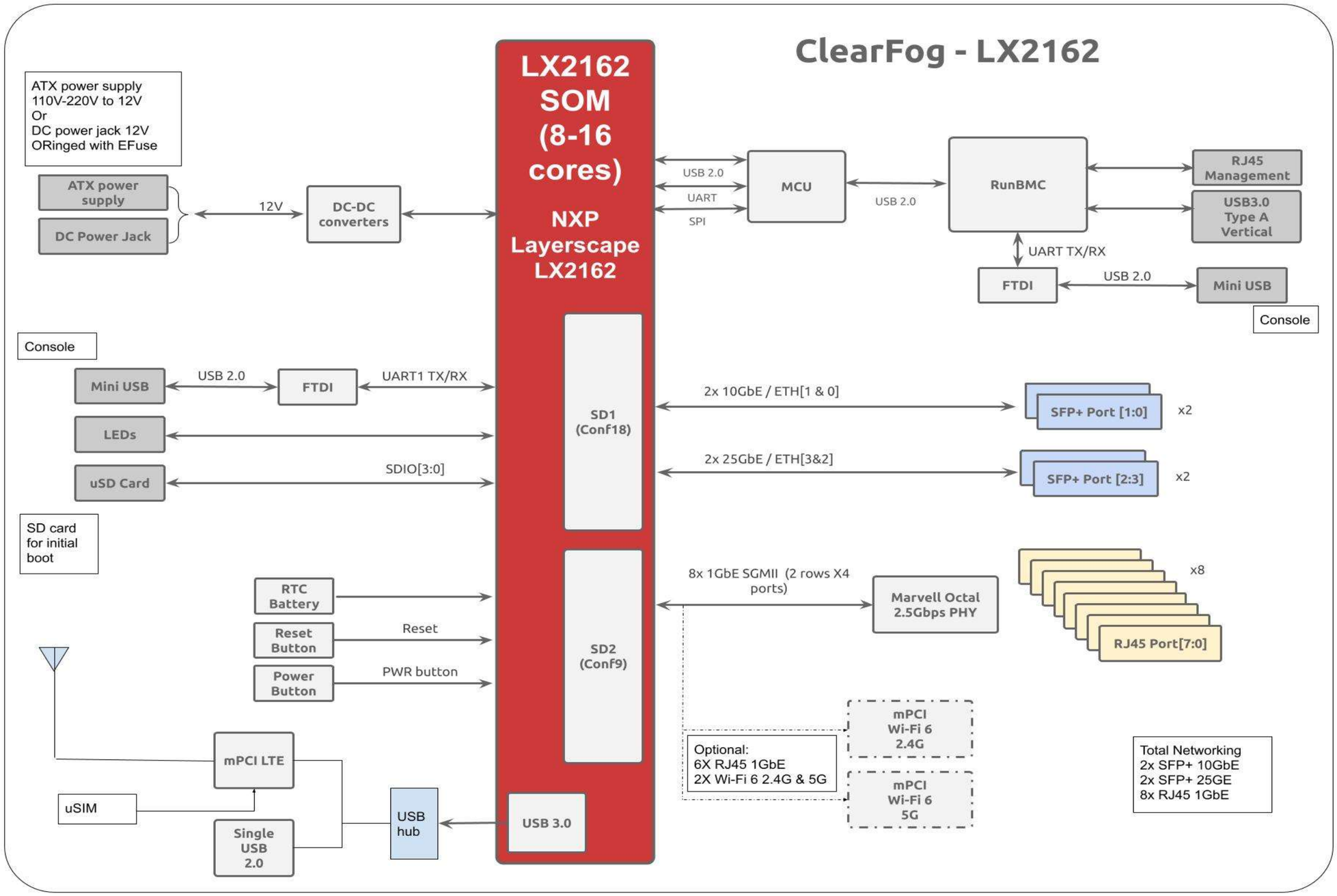
3	RJ45 & PHY	8 port 1GE RJ45 and Marvel Octal PHY
4	RunBMC	RunBMC Management



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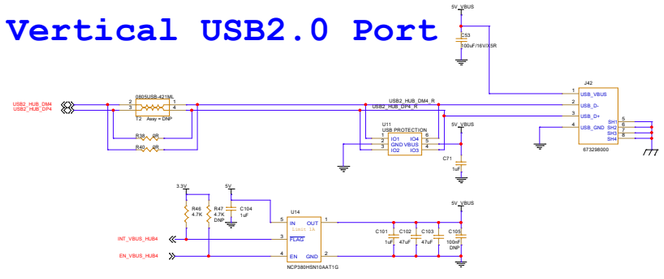
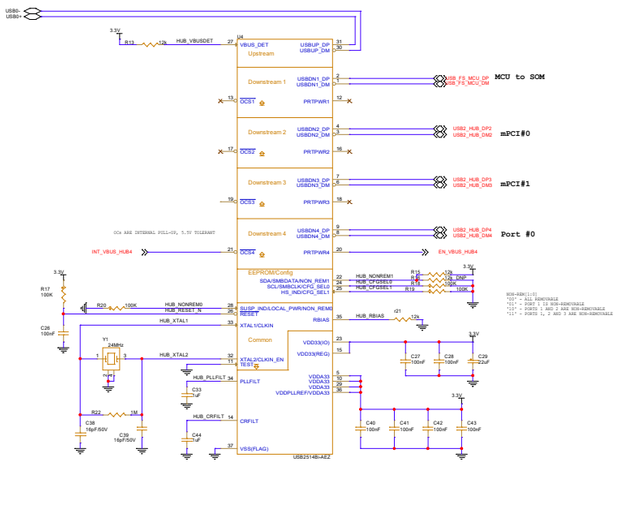
SOM & SFP

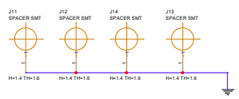
Vertical USB2.0 Port



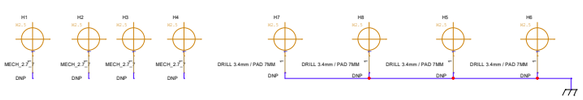
USB2.0 HUB



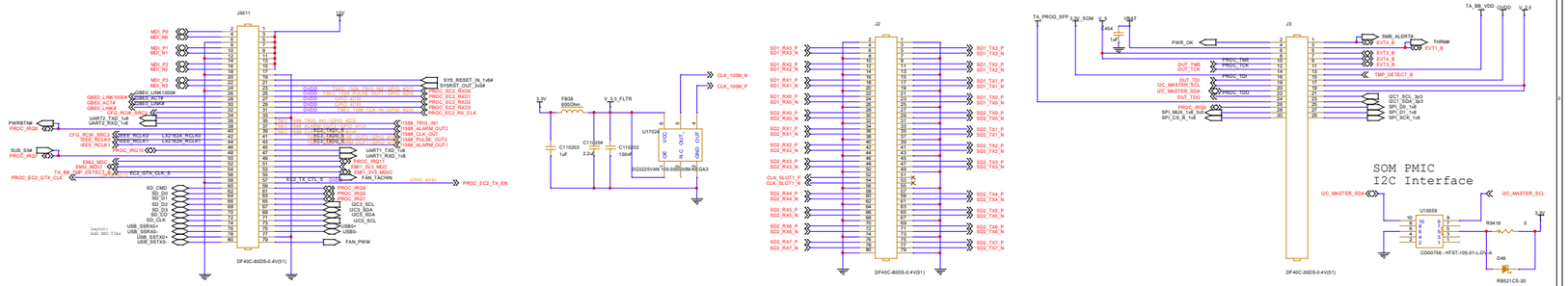
SOM Spacers



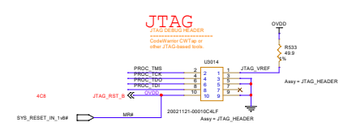
Mechanical Holes



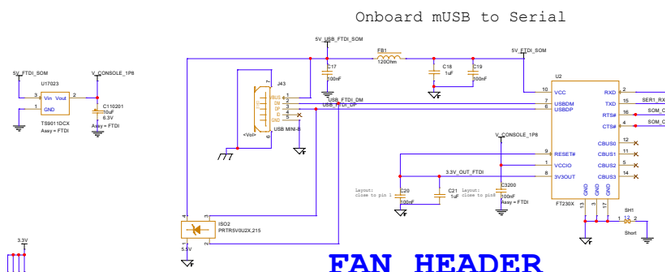
LX2162-SOM Interface



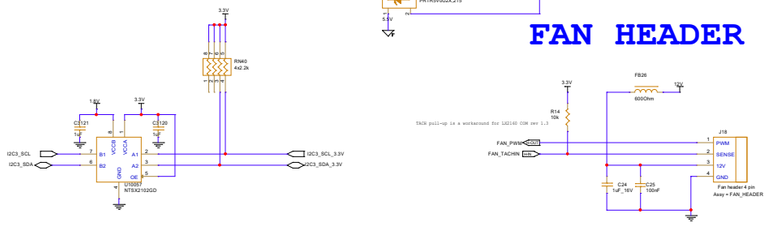
JTAG



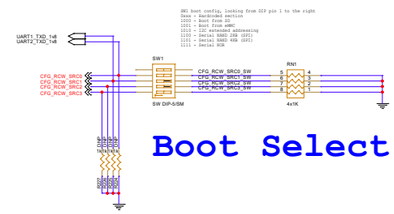
Onboard mUSB to Serial



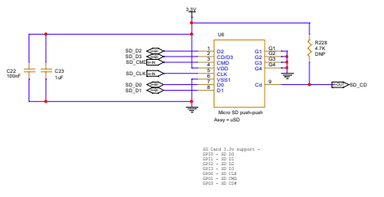
FAN HEADER



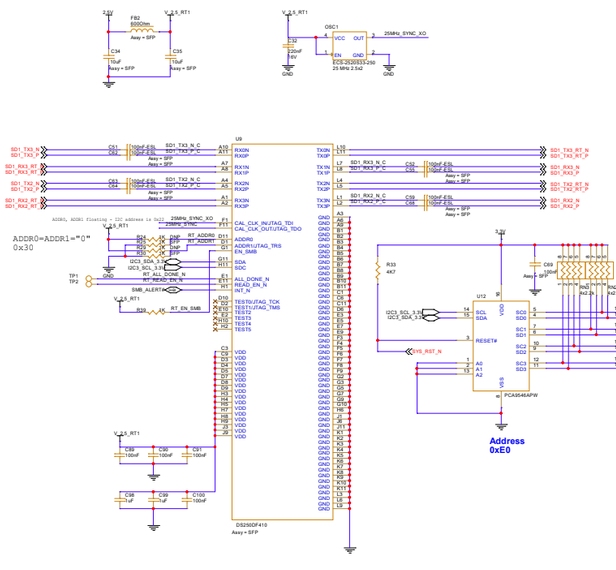
Boot Select

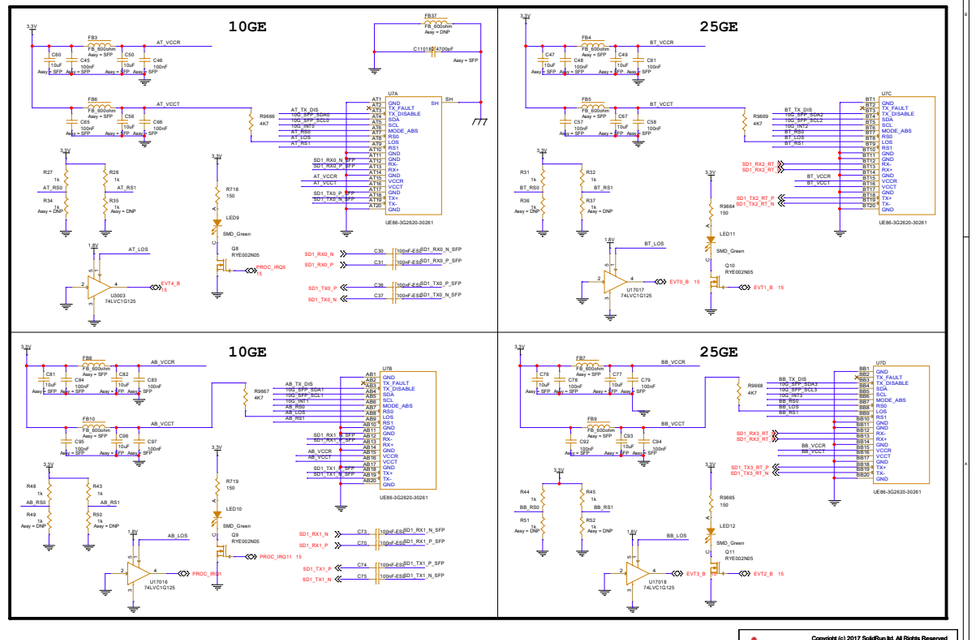


micro SD connector (bootable)



SFPs





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SolidRun

Size	Title	Rev
E	SOM & SFPs	01
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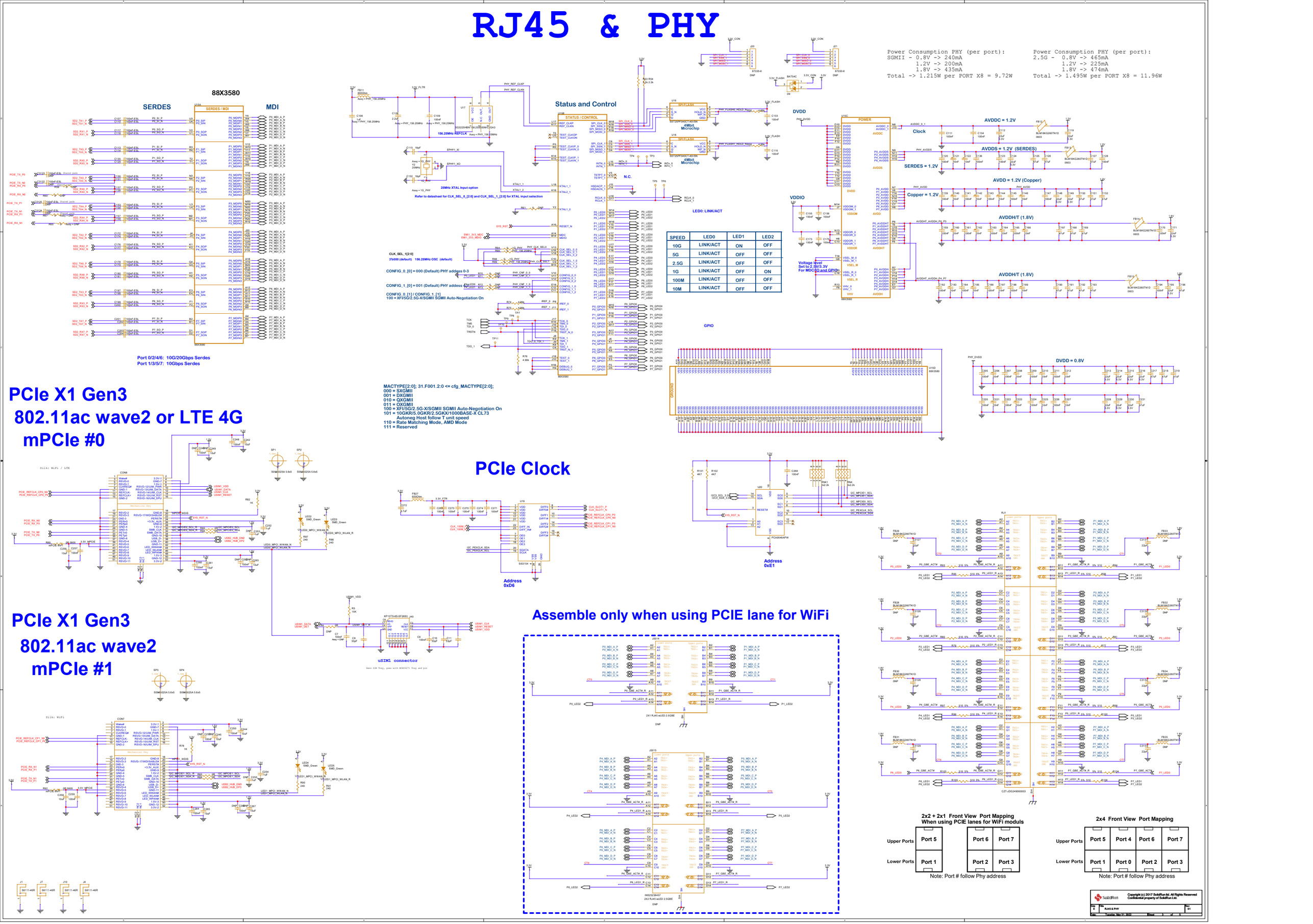
History Revision

00	First Release

RJ45 & PHY



PCIe X1 Gen3
802.11ac wave2 or LTE 4G
mPCIe #0

PCIe Clock

PCIe X1 Gen3
802.11ac wave2
mPCIe #1

Assemble only when using PCIE lane for WiFi

2x2 + 2x1 Front View Port Mapping When using PCIE lanes for WiFi moduls

Upper Ports	Port 5	Port 6	Port 7
Lower Ports	Port 1	Port 2	Port 3

Note: Port # follow Phy address

2x4 Front View Port Mapping

Upper Ports	Port 5	Port 4	Port 6	Port 7
Lower Ports	Port 1	Port 0	Port 2	Port 3

Note: Port # follow Phy address

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File	RJ45 & PHY	Rev
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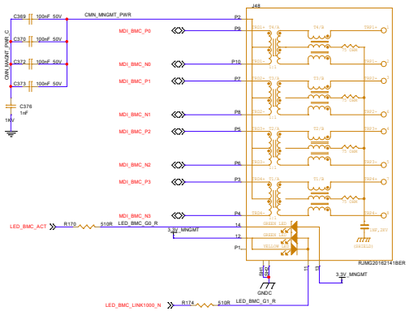
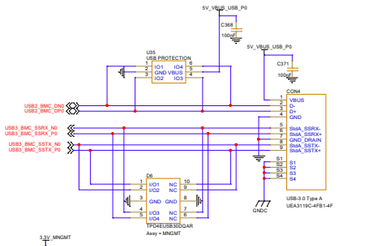
RUNBMC

RJ45

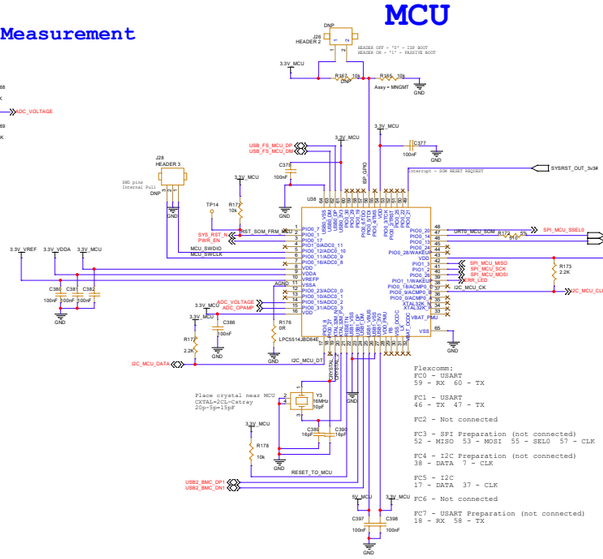


Vertical USB3.0

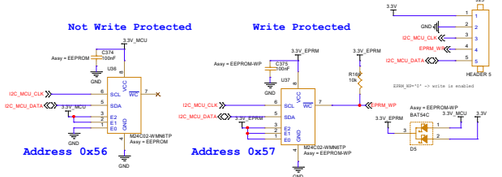


Voltage Measurement

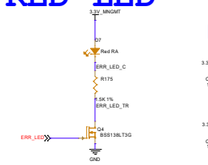
MCU



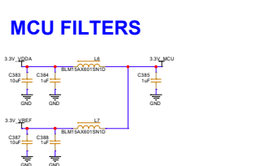
2Kbit EEPROMS-MCU



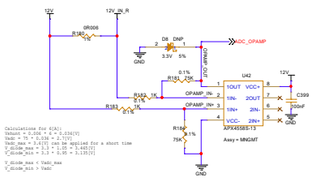
RED LED



MCU FILTERS

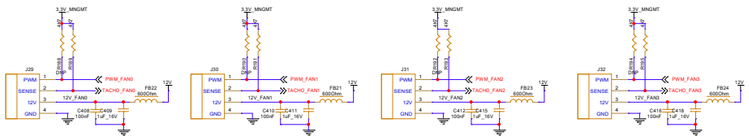


Power Measurement

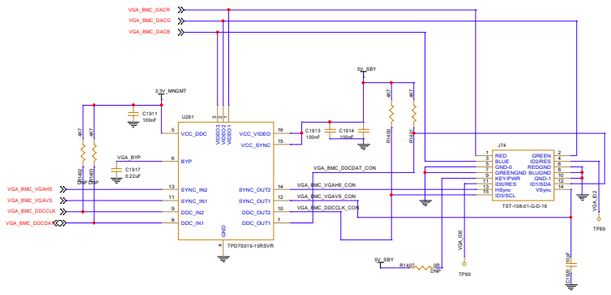


ATX PWR Board MNGMT

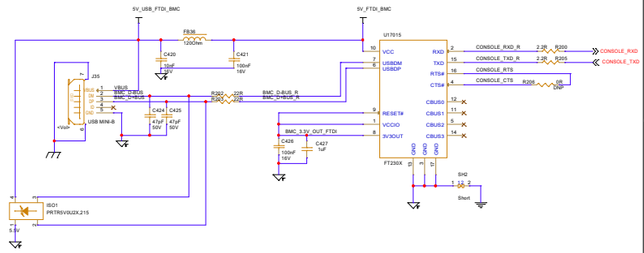
FAN Connectors



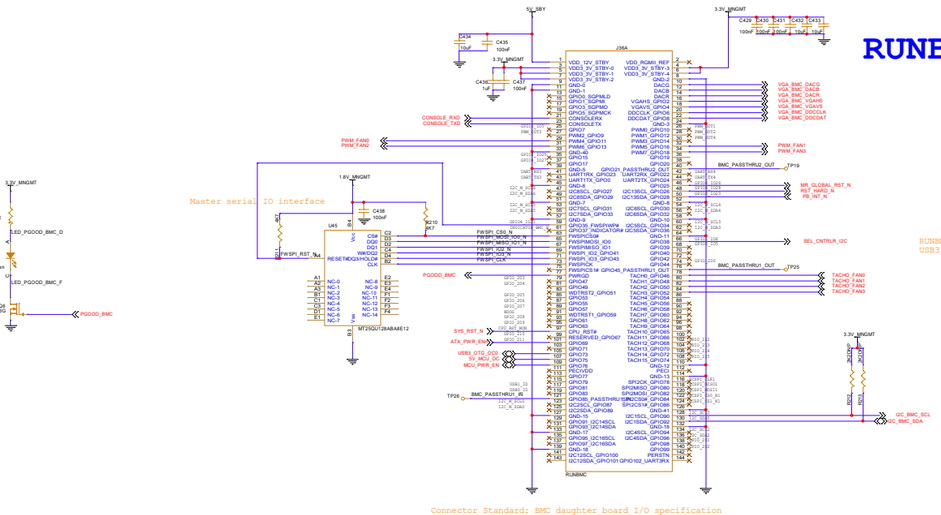
VGA



Console



RUNBMC Connector



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Size	File	Rev
E	RunBMC	01
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