

SolidNET DPU

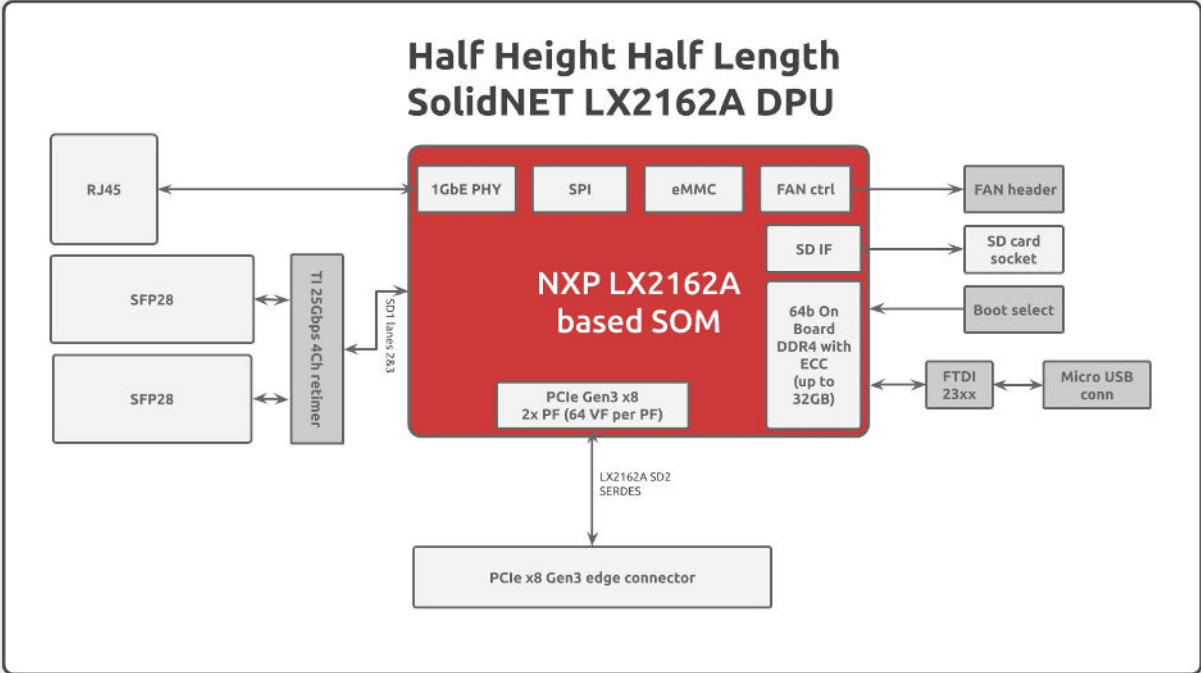
Software Defined DPU, up to 16 x Arm Cortex-A72, PCIe Gen3 x8



- Ideal for isolating of Compute and Infrastructure
- 2x 25GbE + 1x 1GbE (Management)
- Standard virtIO host drivers
- NVMe PF for SSD emulation to remote/local storage
- Half Length Half Height (HHHL) Add-in (TDP <25W)



| SolidNET DPU | |
|------------------------------------|---|
| Form Factor | Half Length Half Height (HHHL) PCIe Add-in card |
| SoC | NXP Layerscape LX2162A |
| Cores | 16 Cores of Arm® Cortex®- A72 Optional 8 and 12 core configurations |
| Memory | 8GB and 32GB options Onboard DDR4 + ECC |
| Interface Standard | PCIe Gen 3.0 X8 Gold Fingers |
| Network | 2x 25GbE 1x 1GbE (Management) |
| Physical/ Virtual Functions | 2 PF with up to 64 VF per PF Default configuration: <ul style="list-style-type: none">• PF0 virtIO Network• PF1 NVMe storage device |
| Security | Acceleration: TRNG, AES, SHA-256, etc. |
| Power Consumption | Max 40W; TDP <25W |



Ordering Information

| Available on Shop | |
|-------------------------|--|
| SKU | Description |
| SRX2162S00D08GE008N01CH | SolidNet LX2162A 8GB RAM 8GB eMMC Com.Temp R01 |
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- To choose your ideal configuration see our [Product Configurator Tool](#)

For more information about the SolidNET DPU
Software/Hardware visit our:

Developer Center